

# Marco Rocchetto

## Curriculum Vitae



### Personal information

Surname / First name	Rocchetto Marco
Address	Verona, Italy
Email	marco.rocchetto@gmail.com (personal), marco@v-research.it (work)
Personal website	<a href="http://marcorocchetto.eu">http://marcorocchetto.eu</a>
Nationality	Italian
Date of birth	6 November 1985

### Work experience

currently since February 2020	Co-founder of V-Research ( <a href="https://v-research.it">https://v-research.it</a> )
May 2017 - December 2019	Senior Engineer - United Technologies Research Center (UTRC), United Technologies Corporation (UTC) Member of the Security Team of the Formal Methods Group in ALES (Advanced Laboratories of Embedded Systems)
November 2016 - April 2017	Postdoctoral Researcher at the University of Luxembourg, SnT (Interdisciplinary Centre for Security, Reliability and Trust) In the context of the COMMA project (Combatting Context-Sensitive Mobile Malware)
October 2015 - October 2016	Postdoctoral Researcher at SUTD (Singapore University of Technology and Design) In the context of the ASPIRE project (Advancing Security of Public Infrastructure using Resilience and Economics), T4: Attacker Models definition
2014	Contract work - Penetration tester, ISGroup S.r.l.
2014	Contract work - Design and implementation of the first prototype of <a href="http://www.dnest.eu">www.dnest.eu</a> , D-Nest Web S.r.l.
January 2011 - September 2015	Research fellow - In the context of the European project FP7 SPaCIoS (Secure Provision And Consumption in the Internet of Services), University of Verona (IT) Title: Development, implementation and use of a platform for the automatic analysis of applications and industrial use cases in the context of the Internet of Services: modeling, validation and developing of the platform
May 2010 - December 2010	Research fellow - In the context of the European project FP7 AVANTSSAR (Automated VALIDatioN of Trust and Security of Service-oriented ARchitectures), University of Verona (IT)

Title	Development, implementation and use of a platform for the automatic analysis of Service Oriented Architecture
<b>Education</b>	
2011-2014	Ph.D (Doctor Europaeus) program in Computer Science - XXVI cycle Graduate School in Sciences Engineering Medicine
Dissertation	Automated reasoning for the verification of security protocols and web applications. Combining Craig's interpolation and the Dolev-Yao intruder model
Supervisor	Prof. Luca Viganò
Committee	Eike Ritter, Santiago Escobar, Silvio Ranise, Nicola Bombieri
February-April 2013	Visiting student at the Swiss Federal Institute of Technology in Zürich (ETH Zürich) - CooperInt 2012 grant
2010	State professional qualifying examination in engineering University of Brescia
10 December 2009	Master's degree in Computer Science (2 years) at the faculty of Mathematical, Physical and Natural Sciences of the University of Verona
Thesis	Analysis, design and development of a web platform for the automatic protocol analyzer AVISPA. Study and implementation of techniques to ensure the security properties in service-oriented architecture
12 December 2007	Bachelor's degree in Computer Science (3 years) at the Faculty of Mathematical, Physical and Natural Sciences of the University of Verona
Thesis	Functionality and use of IPSecurity
Supervisor	Prof. Luca Viganò
2007	Stage at University of Verona, working for the tvblob project ( <a href="http://www.tvblob.com">www.tvblob.com</a> )
29 June 2004	Diploma at Liceo socio-psico-pedagogico (human sciences), C.Montanari - Verona